Amendments to the Claims:

This listing of claims will replace all prior versions, and listings, of claims in the application:

Listing of Claims:

1.   (Currently Amended) A method for debugging application programs, the method comprising:

allocating prescribed regions of a system memory for each of a plurality of application programs;

writing application identifier information on an application program to be performed;

checking whether the application program is performed in a designated region; and

generating an interrupt signal when the application program is performed in a region other than the designated region for the application program, wherein the checking whether the application program is performed in a designated region further comprises,

checking an operation region of a current application program being accessed and outputting a result,

generating an address signal corresponding to the checking result, and

outputting a grant signal based on the address signal.

2.   Canceled

3.   (Currently Amended) The method according to claim ~~2~~ 1, further comprising:

latching a data signal corresponding to the written application identifier

information on the application program; and

outputting an application signal corresponding to the application identifier that is

identified based on the latched data signal.

4.   (Currently Amended) The method according to claim 3, wherein the designated

region is ~~an~~ the operation region assigned to each application program.

5.   (Previously Presented) The method according to claim 1, further comprising:

determining the information on the application responsive to the interrupt signal;

and

performing an operation corresponding to the information on the application

based on the determination.

6.   (Previously Presented) The method according to claim 5, further comprising

sending a control signal that is generated based on the interrupt signal to the system memory.

7.   (Previously Presented) The method according to claim 1, wherein after an

application program switching occurs to a next application, a checking process based on

information corresponding to the next application is repeatedly conducted to check whether the

next application is performed in a corresponding designated region.

8.   Canceled

9.   (Currently Amended) The method according to claim 8 1, wherein a task signal is

used for outputting the grant signal.

10-14.   Canceled

15.   (Currently Amended) A An apparatus for debugging application programs, the

apparatus comprising:

first control means for writing an application identifier provided for a plurality of application programs to be performed, for generating a data signal corresponding to the application identifier, and for activating selected application programs;

checking means for outputting an application signal corresponding to the application identifier that is identified based on the data signal, and for generating an interrupt signal according to a determination whether a current application program is performed in a designated region;

storage means for writing the application identifier provided by the first control means, and for assigning a corresponding designated operation region for each of the plurality of applications in said storage means; and

second control means for outputting a control signal to control the storage means based on the generated interrupt signal.

16. Canceled

17. (Previously Presented) The apparatus according to claim 15, wherein an address signal is used as a basis of determining whether the current application is performed in the designated region.

18.   (Previously Presented) The apparatus according to claim 15 wherein the task checking means comprises:

a latching unit that latches the data signal;

a decoding unit that identifies the application identifier based on the latched data signal, and generates the application signal corresponding to the application identifier; and

a comparing device that receives the application signal and generates the interrupt signal according to the address signal.

19.   (Previously Presented) The apparatus according to claim 18, wherein a plurality of task comparing units is included in the comparing device that are equal in number to application programs to be performed, wherein each comparing unit comprises,

a first logic gate that logically processes the address signal; and

a second logic gate that logically processes the task signal, an output signal of the first logic gate and a first write enable signal to output a grant signal used to generate the interrupt signal.

20.   (Previously Presented) The apparatus of claim 19, wherein the first logic gate is an OR-NOT gate and the second logic gate is an AND gate.

21.   (Currently Amended) The apparatus according to claim 15, wherein the control

signal ~~terminates~~ is for control of a corresponding application program.

22.   (Previously Presented) The method of claim 9, wherein the outputting a grant

signal comprises:

logically processing the address signal, an application signal and a first write enable

signal in each of a plurality of application comparison units equal in number to the plurality of

application programs, and wherein said logically processing comprises,

logically processing the address signal in a first logic gate, and

logically processing the application signal, an output signal of the first logic

gate and a first write enable signal in a second logic gate to output the grant signal used to

generate the interrupt signal.